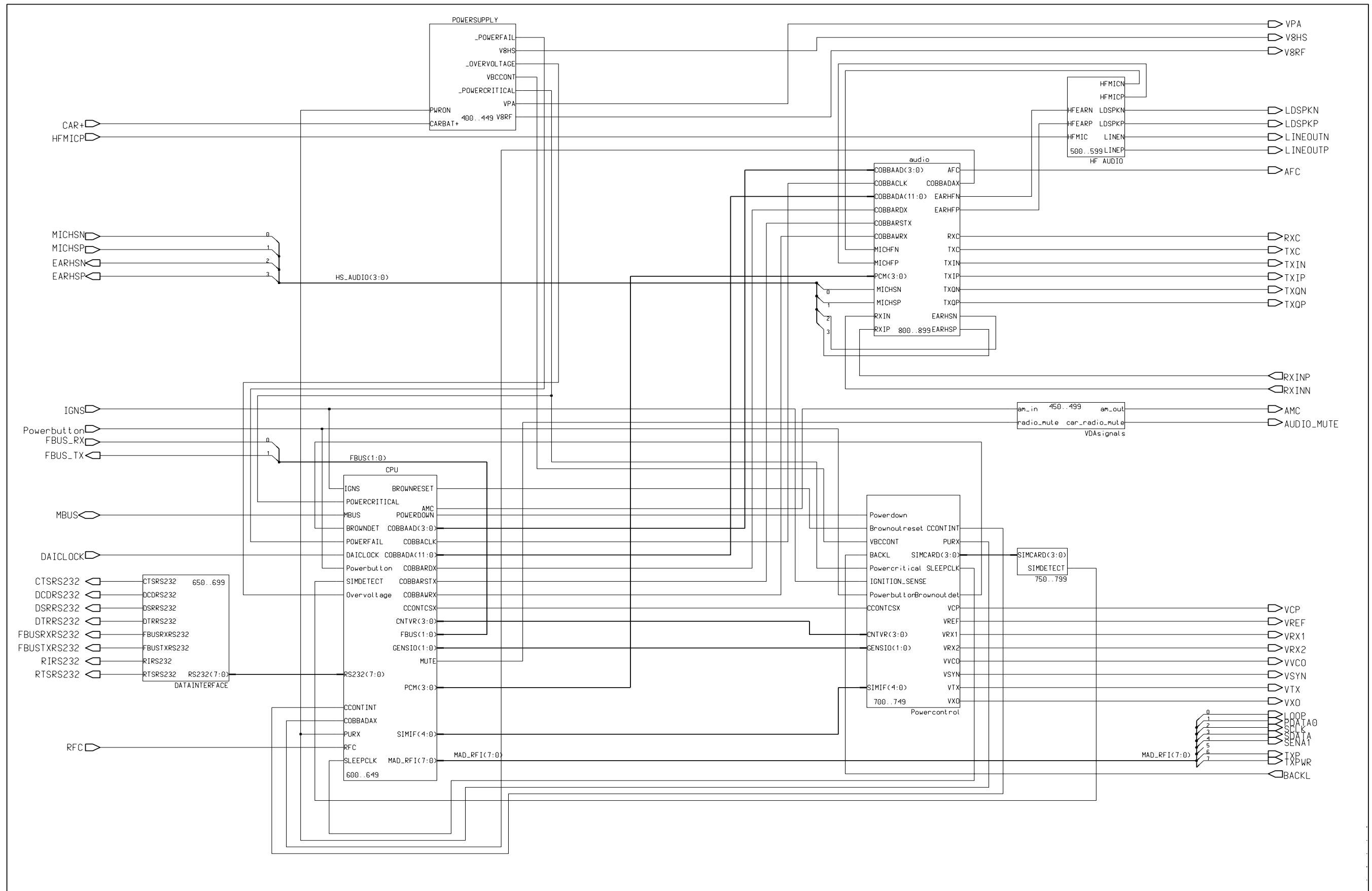
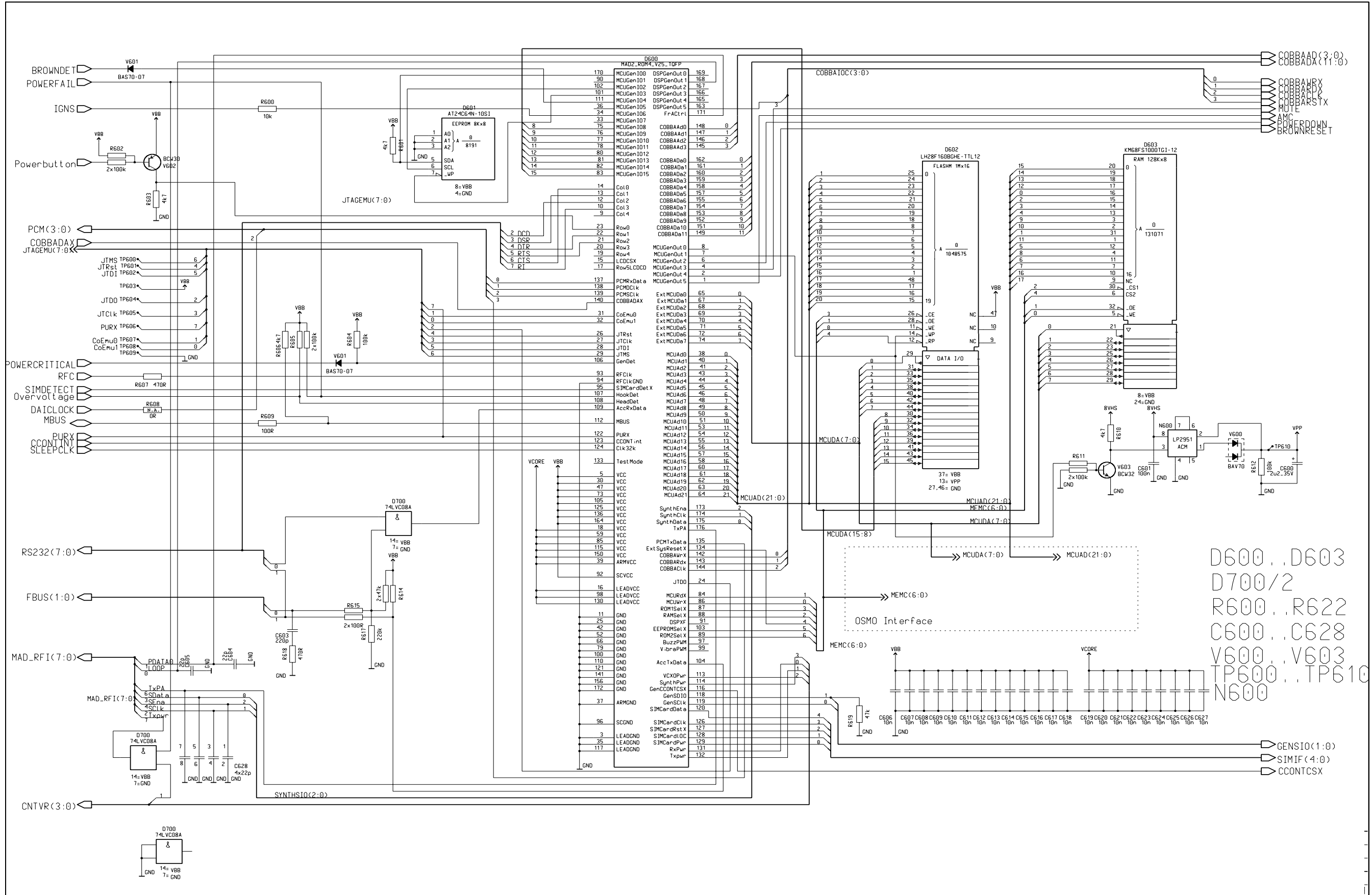


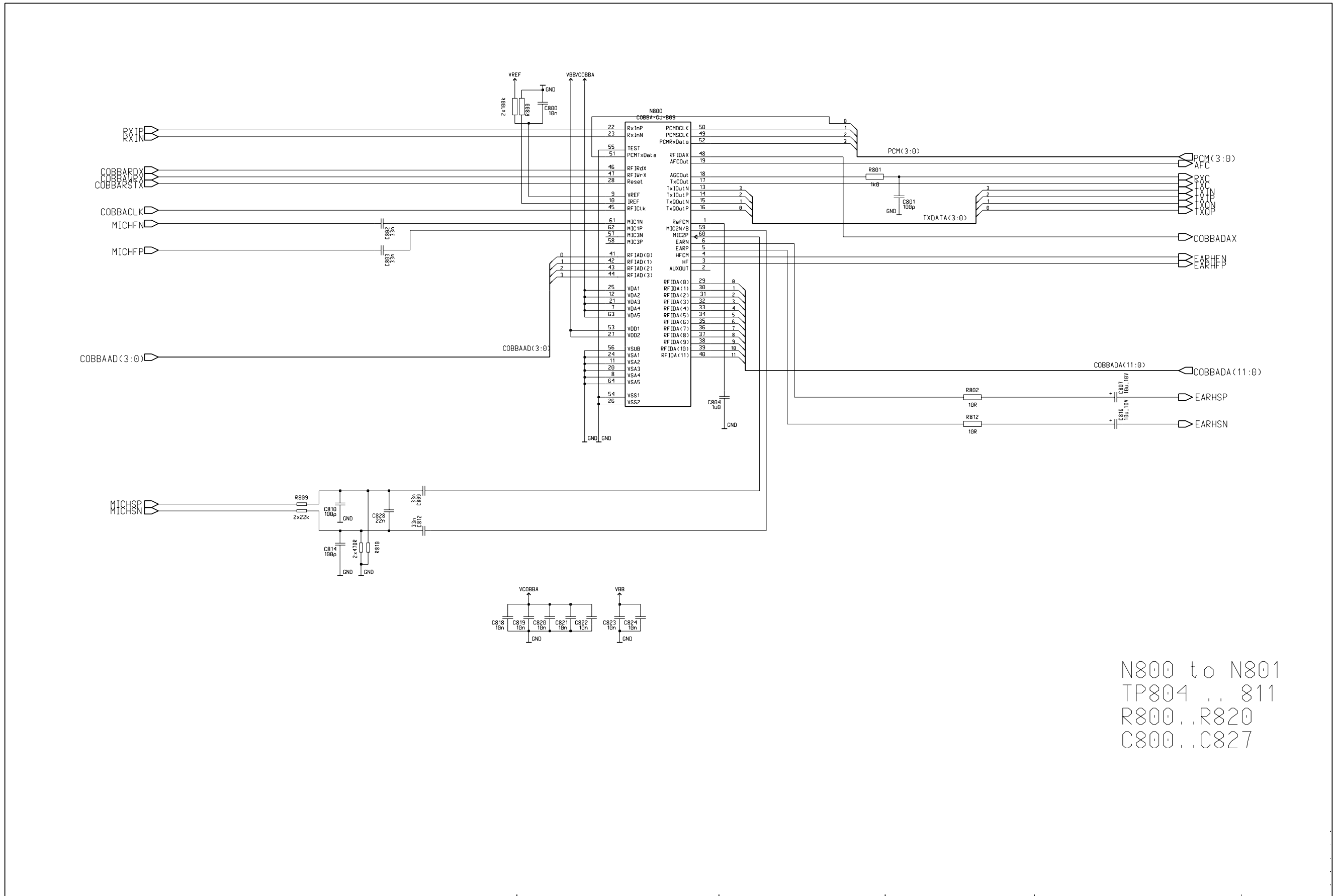
System Block Diagram



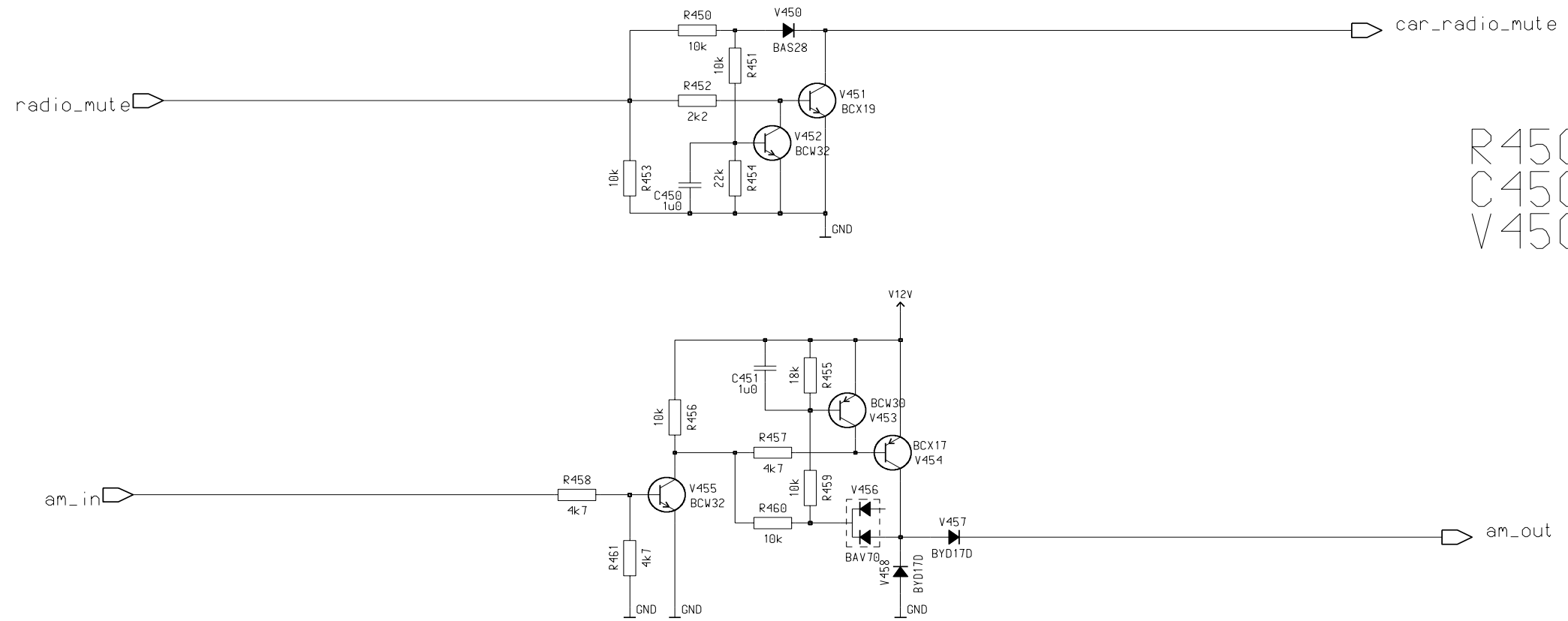
CPU



Audio

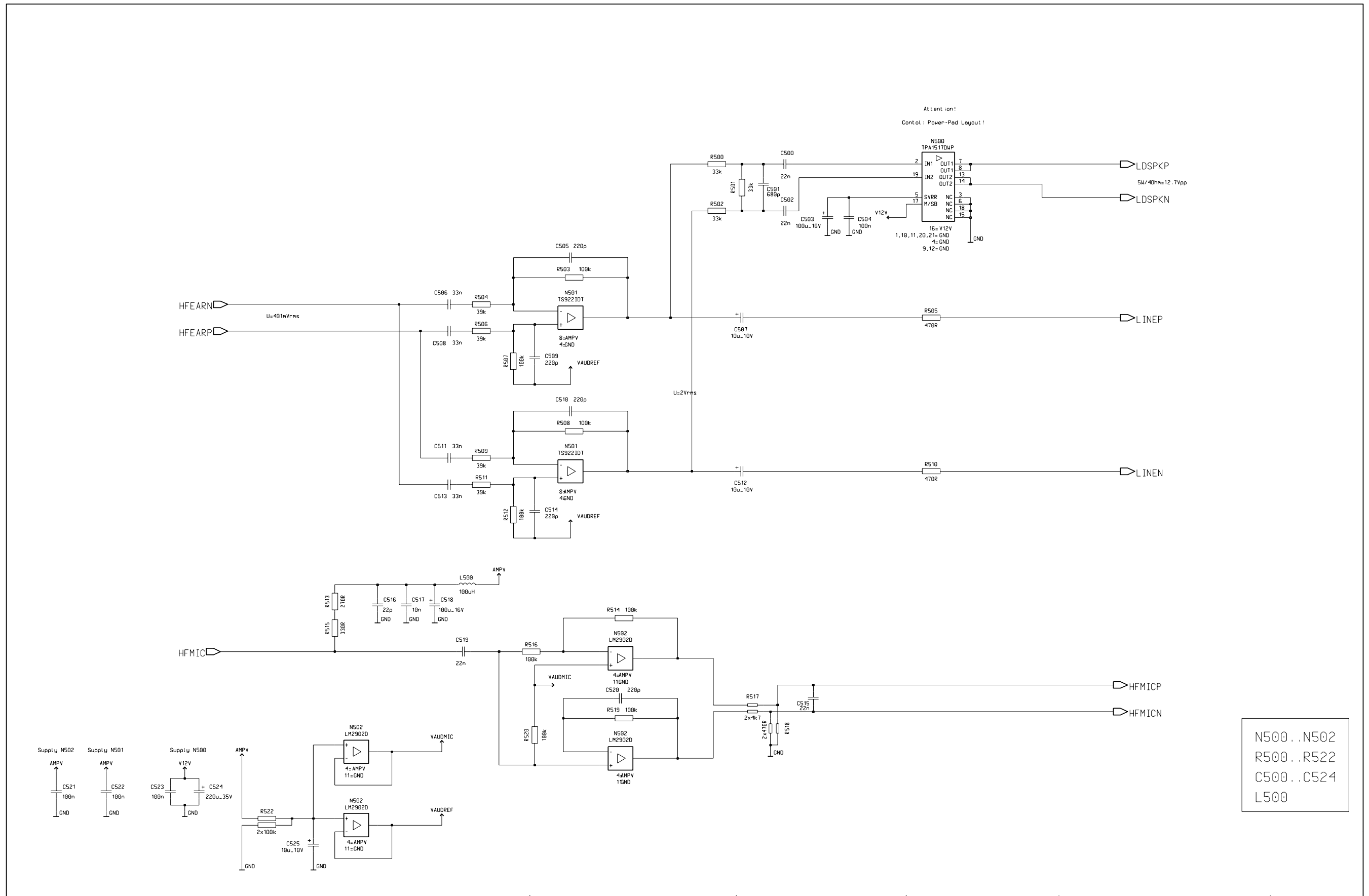


VDA Signal



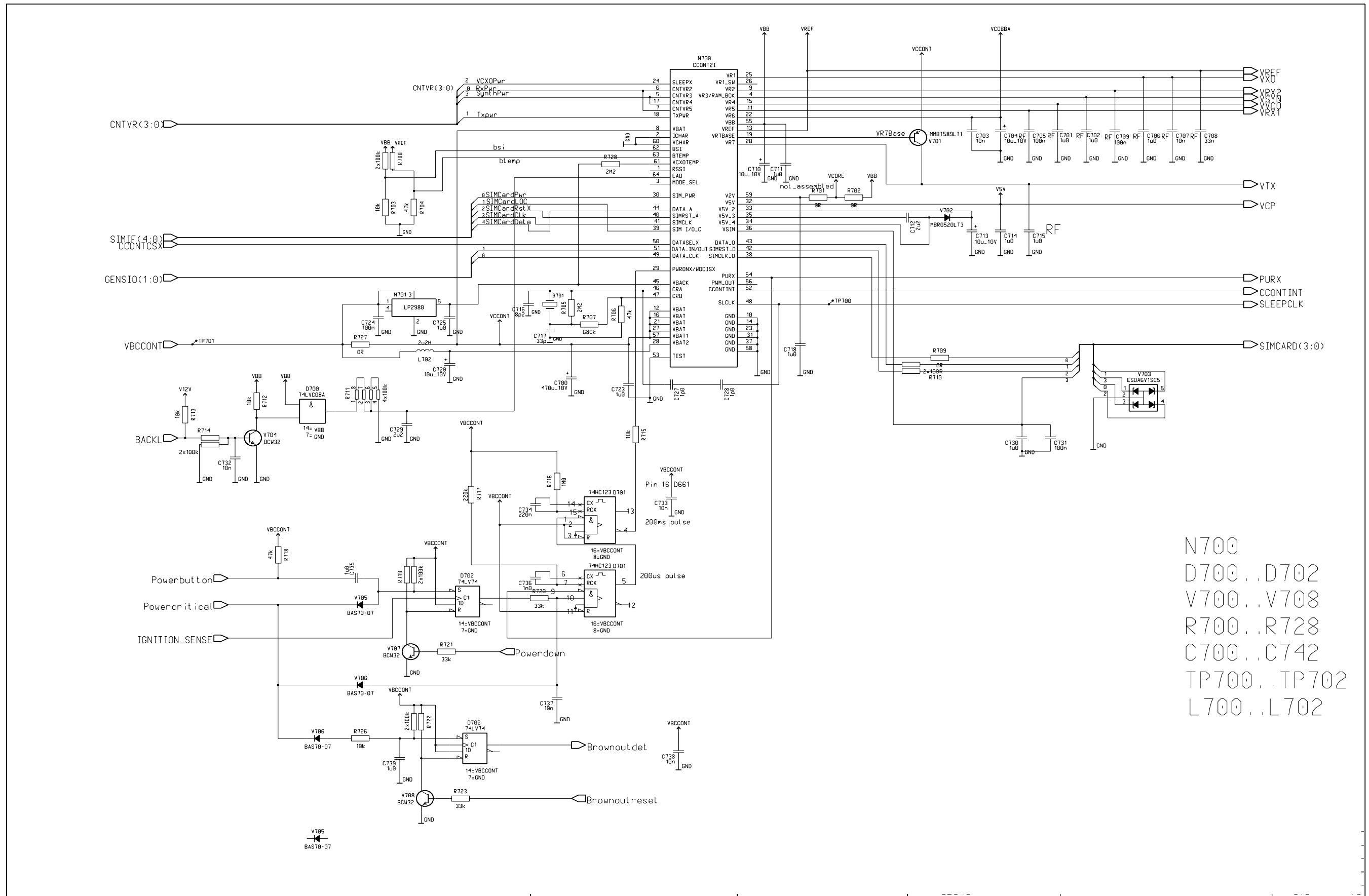
R450 .. R461
 C450 .. C451
 V450 .. V459

HF Audio



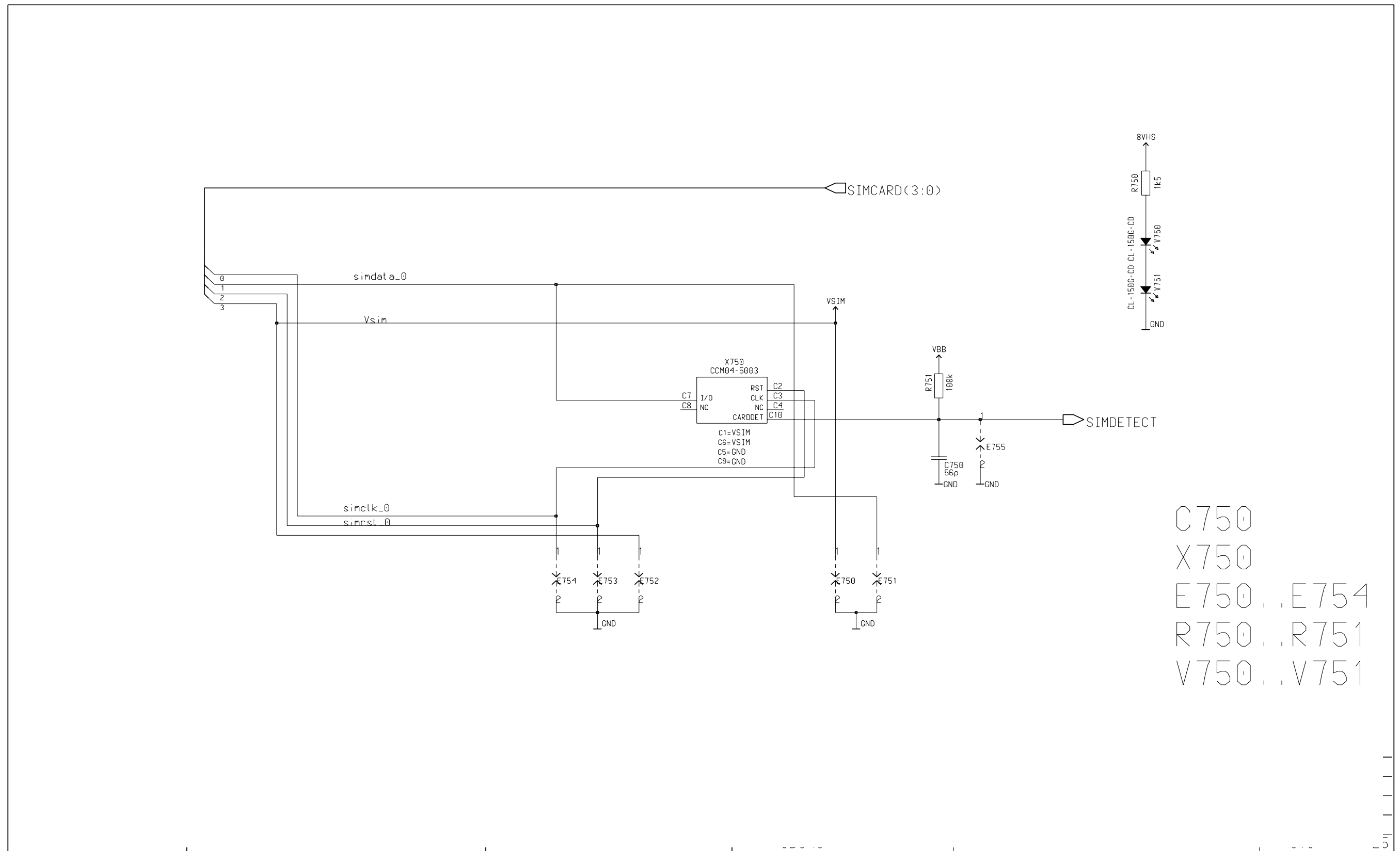
N500..N502
R500..R522
C500..C524
L500

Power Control

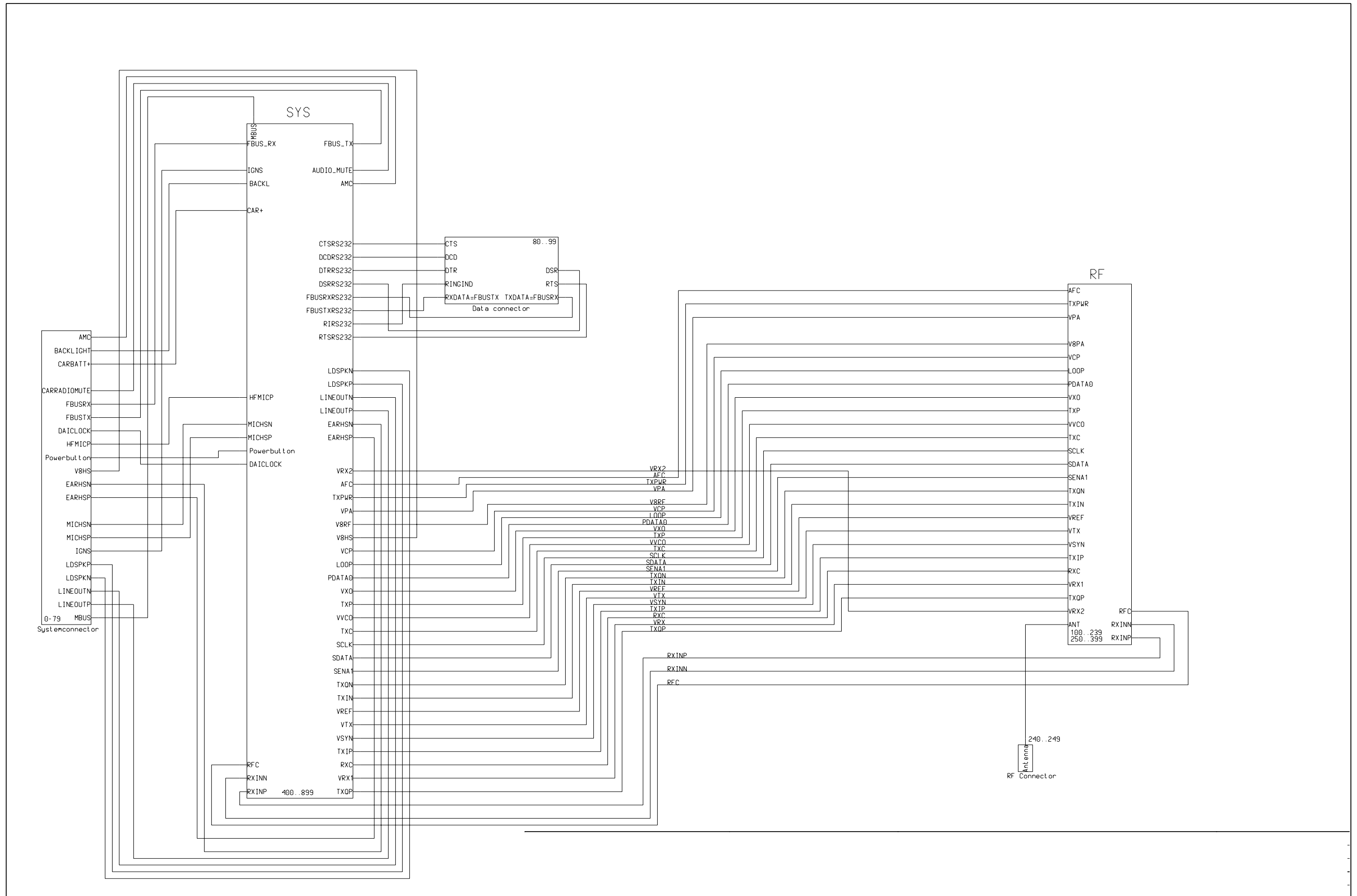


- N700
- D700..D702
- V700..V708
- R700..R728
- C700..C742
- TP700..TP702
- L700..L702

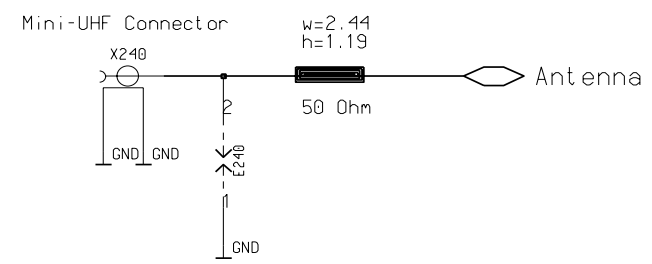
SIM



Interconnections

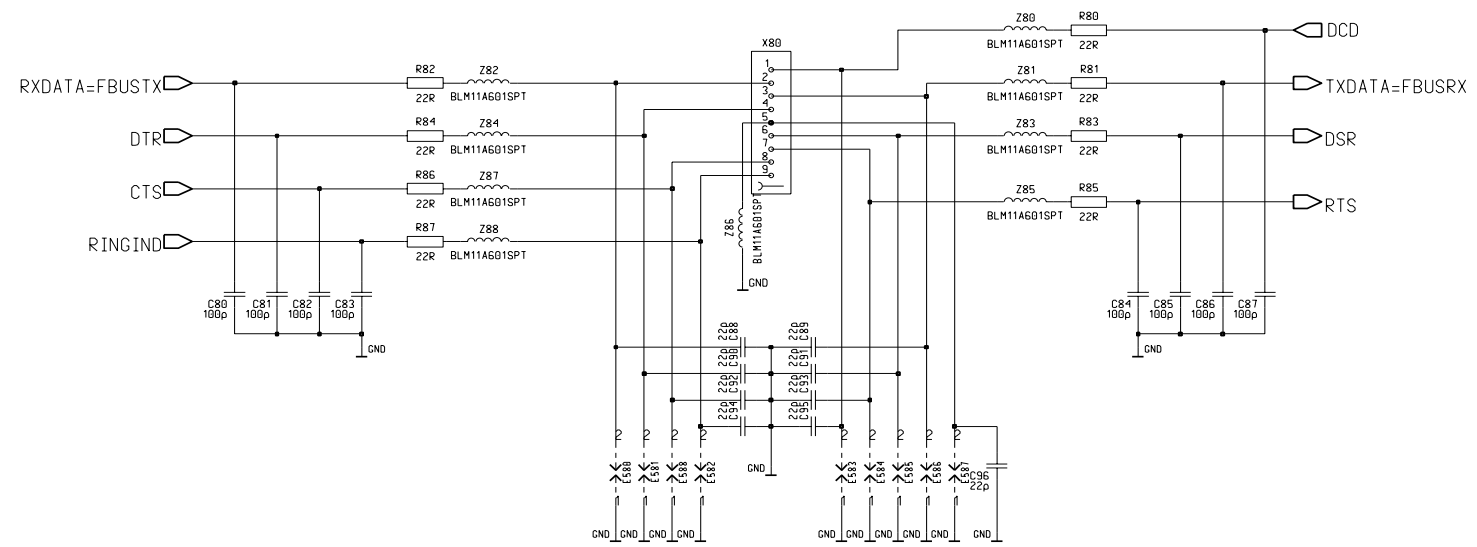


RF Connector

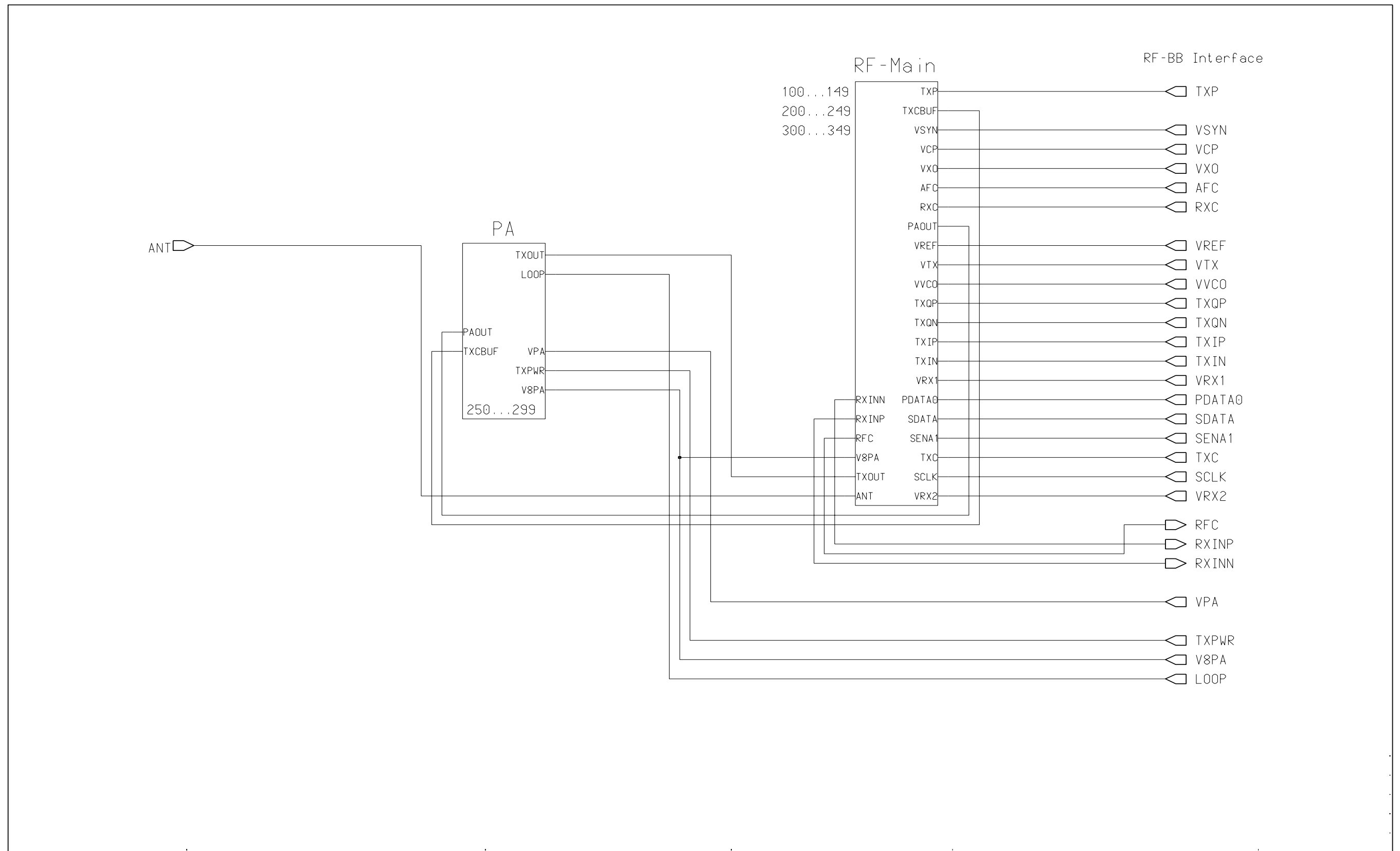


Data Connector

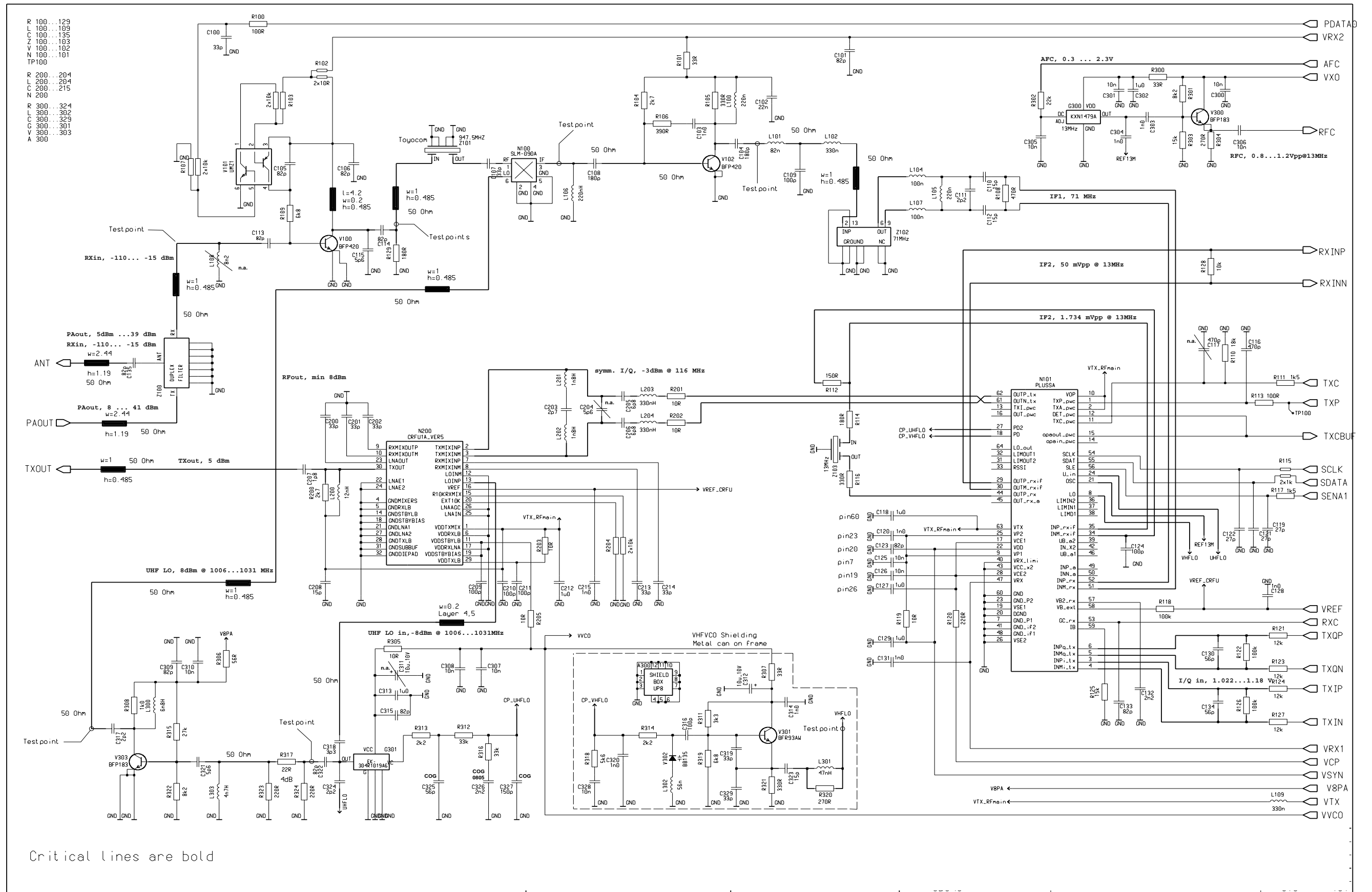
E580 .. E587
 C80 .. C96
 R80 .. R87
 Z80 .. Z88
 X80



RF Block



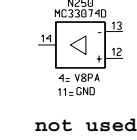
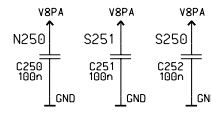
RF Main



Critical lines are bold

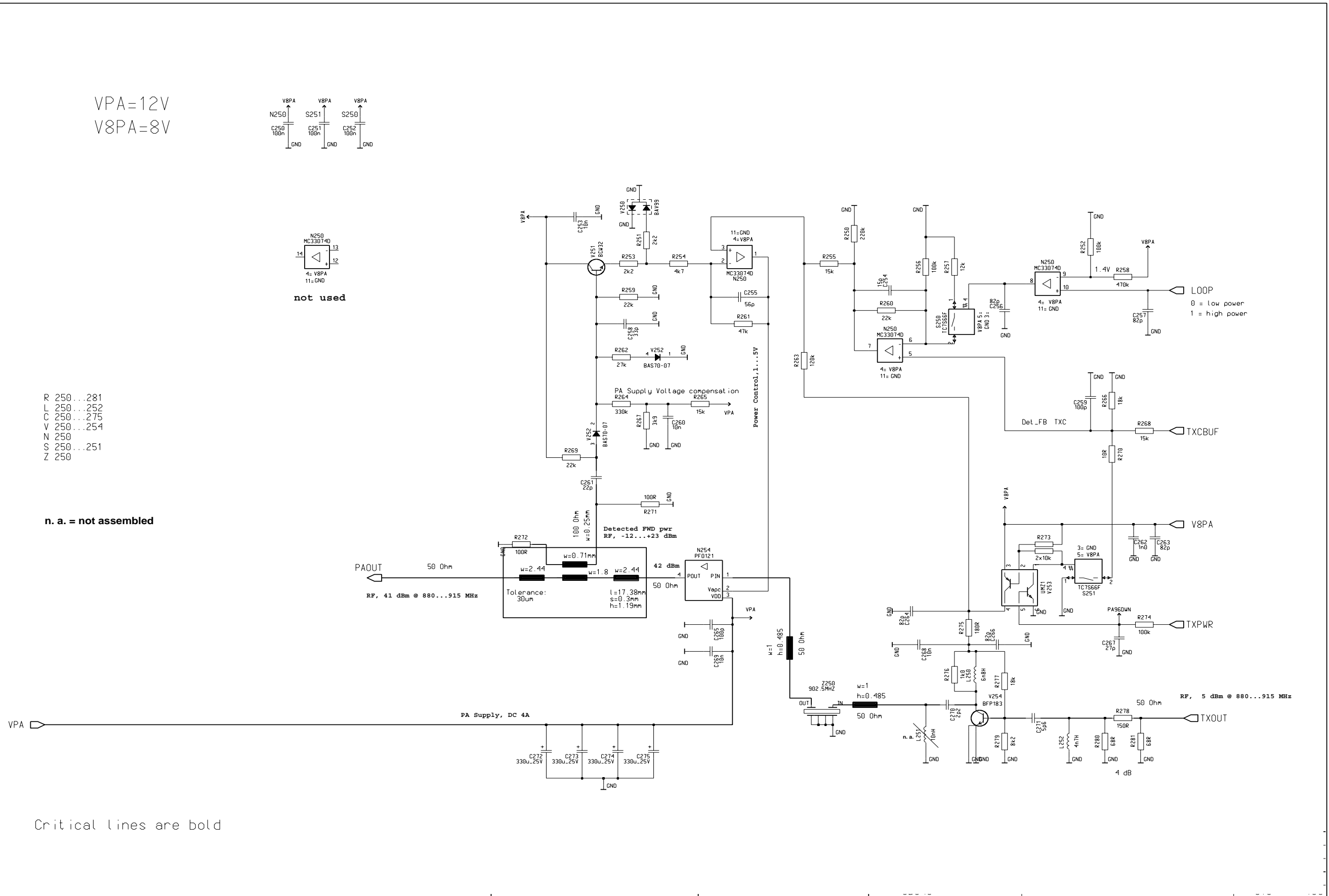
Power Amp

VPA=12V
V8PA=8V



- R 250...281
- L 250...252
- C 250...275
- V 250...254
- N 250
- S 250...251
- Z 250

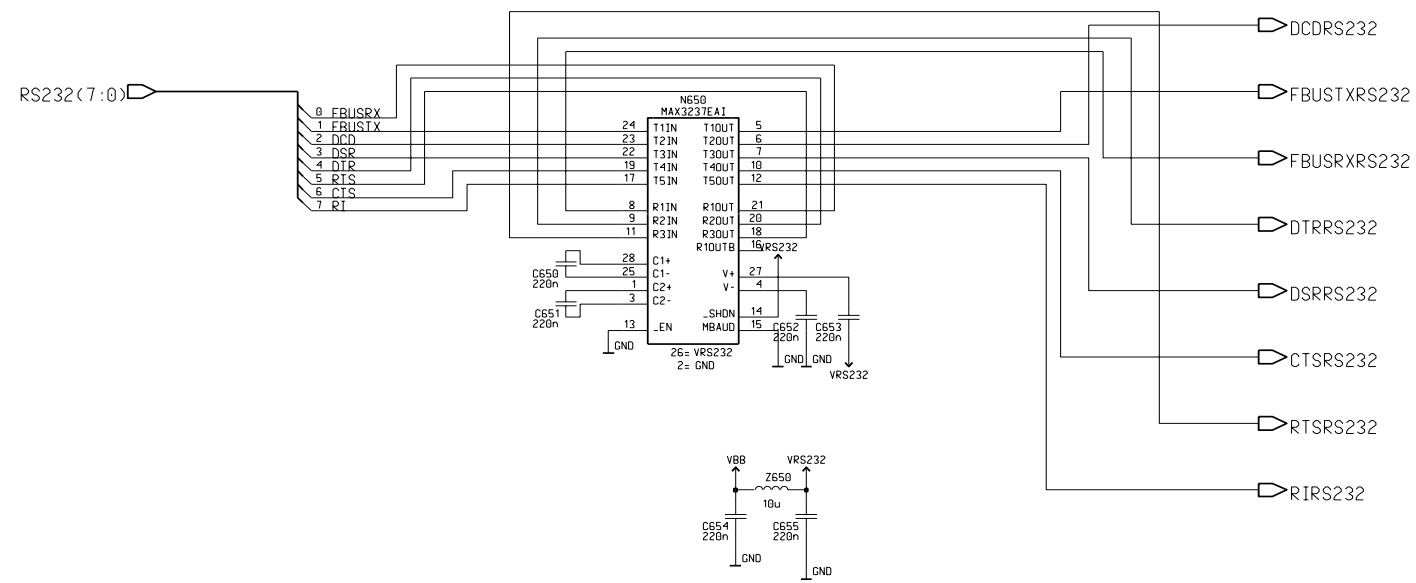
n. a. = not assembled



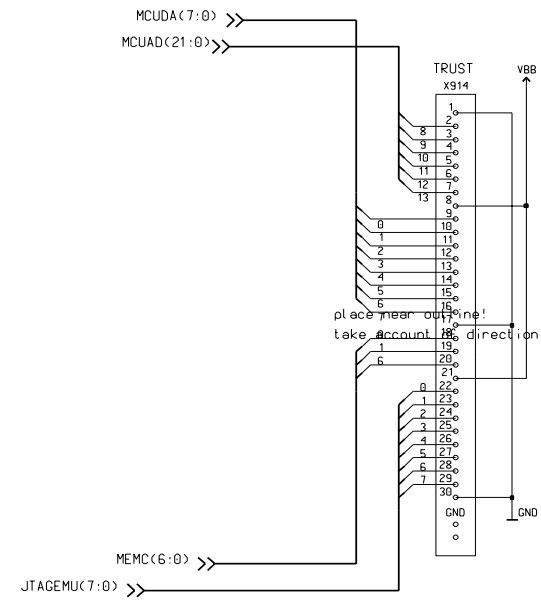
Critical lines are bold

RS232 level Shifter

R650
N650
Z650
C650...C656



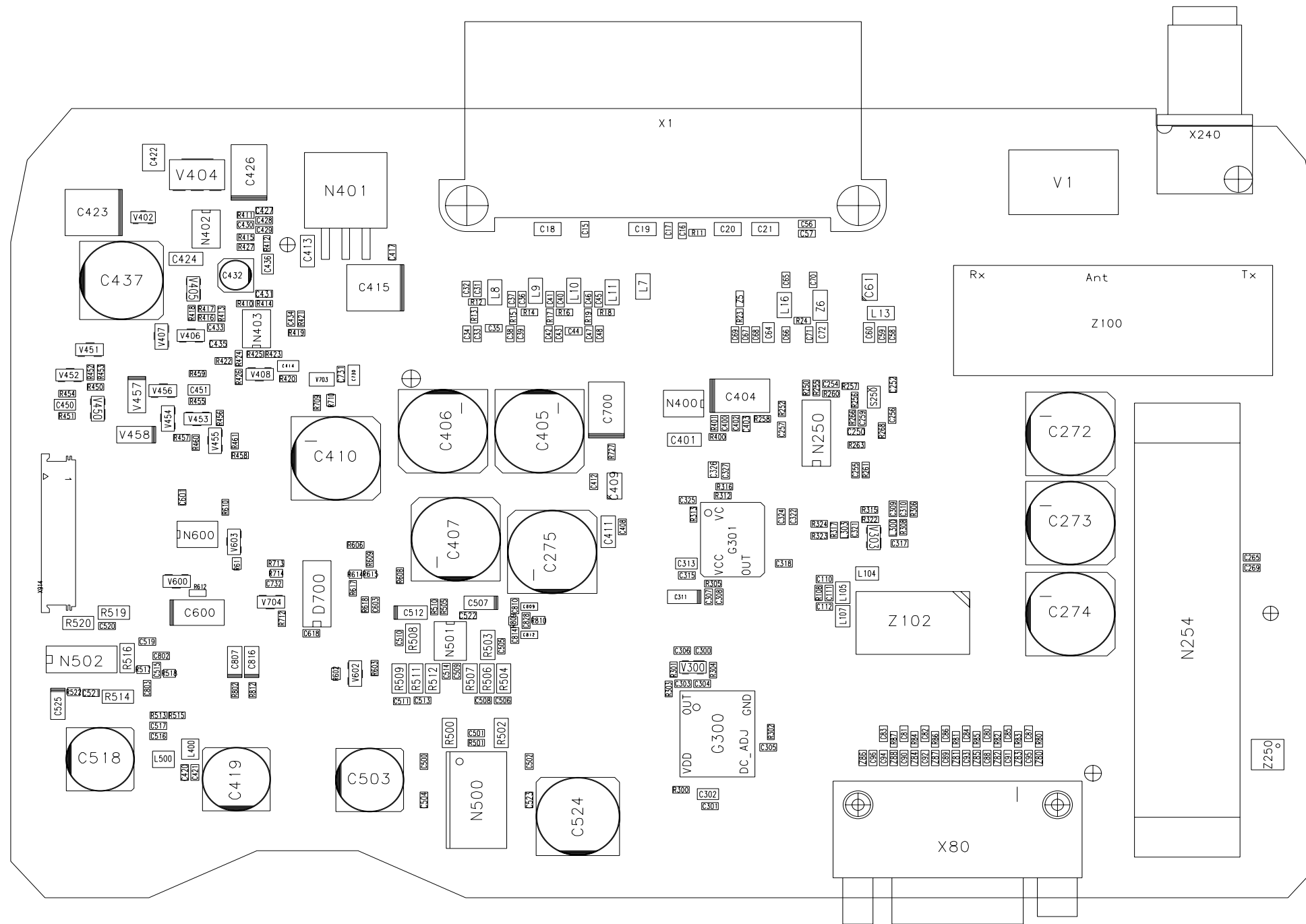
Test Connectors



Component Layout - Top



Component Layout - Bottom



Assembly Values - Top



Assembly Values -Bottom

